Amendments to the Specification

Please replace the paragraph beginning at page 18, line 1, with the following rewritten paragraph:

Fig. 6 illustrates a flow chart of a process for ensuring consistancy of designable applications in a CAD environment in accordance with the present invention. First, a set of design rules defining the physical limitations of the technology with which the design is to be implemented is provided, as illustrated by procedural step 600 of Fig. 6. Typically, the design rules will be defined in a file or files which define specific physical quanties for the physical characteristics of a particular fabrication technology. For example, the width [[of]] or depth of a path of metal on a substrate. It will be appreciated that such design rules will vary greatly according to the technology used to implement a device. Next, in accordance with the invention, a global design rule definition file 200 is defined in which specific values for design rules of a particular technology are assigned to a global variable, as described with reference to section 314 of Fig. 3, and as illustrated in procedural step 602. If a CAD tool is native, i.e. developed in the same language as the global variables are defined in the global design rule definition file 200, the CAD tool accesses the values of the global variables in file 200 directly, as illustrated in step 610, without further need for translation or compilation of a technology file 202. In this case when a global variable is redefined in the global design rule definition file 200 the native CAD tools will directly access the updated variable.

Please replace the paragraph beginning at page 21, line 1, with the following rewritten paragraph:

In the technology file 202, a command to execute SKILL program ParseSkillRules is inserted so as to execute itself right after loading. ParseSkillRules parses each line in the global design rule definition file 200 into the techParameter format, i.e. the format of the non-native CAD tool. For example, if the technology file 202 contains the assignments as set forth below:

```
V1Width = 0.32        ; 6.1 Size
V1ToV1 = 0.52         ; 6.2 Space
M1OverV1 = 0.10       ; 6.3.1 m1 enclosure(in straight lead)
M1WideOverV1 = 0.30 ; 6.3.2 Wide m1(>7.0) enclosure
V1ToCont = 0.0        ; 6.4 Space to cont
```

it will be translated to:

```
~~techParams(~~ techParam(
 (V1Width 0.32)
 (V1ToV1 0.52)
 (M1OverV1 0.10)
 (M1WideOverV1 0.30)
 (V1ToCont 0.0)
)
```